



SDI Review Form 1.6

Journal Name:	British Journal of Mathematics & Computer Science
Manuscript Number:	2013_BJMCS_7539
Title of the Manuscript:	COMPARISON BETWEEN SPARTAN-3E AND VIRTEX-6 TECHNOLOGIES ON FPGA FOR UART TRANSMISSION
Type of the Article	Original Research Paper

General guideline for Peer Review process:

This journal's peer review policy states that **NO** manuscript should be rejected only on the basis of '**lack of Novelty**', provided the manuscript is scientifically robust and technically sound.

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PART 1: Review Comments

	Reviewer's comment	Author's comment (if agreed with reviewer, correct the manuscript and highlight that part in the manuscript. It is mandatory that authors should write his/her feedback here)
<u>Compulsory</u> REVISION comments	➤ Mentioned that Xilinx Xpower tool used for power analysis, but not provided power analysis in this paper ➤ Include power analysis of designed or proposed circuit.	Power analysis is beyond part of this paper, that's why we are excluding this term and it is included in the next part of this study.
<u>Minor</u> REVISION comments	✓ In this paper in conclusion part mentioned that "improved architecture can enhance the speed of UART by 4 times" ✓ But results mentioned in Table 1 indicating speed improves by 2.5 times only. ✓ Review the statement and the results	Some minor correction have been made in the conclusion Part and also in the table.1, now , the results are showing that improved architecture can enhance the speed of UART by 3 times.
<u>Optional/General</u> comments	❖ Explanation of various signals of Block diagram shown in Figure 2 is required for more clarity	Flow chart is self explanatory so, that's why its explanation is not included in the theory part.