



SDI Review Form 1.6

Journal Name:	British Journal of Mathematics & Computer Science
Manuscript Number:	2013_BJMCS_7539
Title of the Manuscript:	COMPARISON BETWEEN SPARTAN-3E AND VIRTEX-6 TECHNOLOGIES ON FPGA FOR UART TRANSMISSION
Type of the Article	Original Research Paper

General guideline for Peer Review process:

This journal's peer review policy states that **NO** manuscript should be rejected only on the basis of '**lack of Novelty**', provided the manuscript is scientifically robust and technically sound.

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PART 1: Review Comments

	Reviewer's comment	Author's comment (if agreed with reviewer, correct the manuscript and highlight that part in the manuscript. It is mandatory that authors should write his/her feedback here)
<u>Compulsory</u> REVISION comments	In this paper, the author(s) presents the hardware implementation of high speed and power efficient (Universal Asynchronous Receiver/Transmitter) UART using (Field Programmable Gate Array) FPGA. However, there are few explanations of the rationale for the study design, and the conclusions are overstated. In addition, the list of references is completely correct. And it is noted that the manuscript needs careful editing by someone with expertise in technical English editing paying particular attention to English grammar, spelling, and sentence structure so that the goals and results of the study are clear to the reader.	Power analysis is the beyond part of this study so it is excluded from this paper. Some minor improvements in the grammar are done accordingly.
<u>Minor</u> REVISION comments		
<u>Optional/General</u> comments		